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| APPLICATION NO.   | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|-------------|----------------------|---------------------|------------------|
| 09/574,327  | 05/19/2000  | Albert Tung-chu Man  | 0100.00000710       | 8261             |
| 24228   | 7590        | 11/03/2004           | EXAMINER            |                  |
| MARKISON & RECKAMP, PC<br>PO BOX 06229<br>WACKER DR<br>CHICAGO, IL 60606-0229 |             |                      | ALPHONSE. FRITZ     |                  |
|   |             |                      | ART UNIT            | PAPER NUMBER     |
|   |             |                      | 2133                |                  |

DATE MAILED: 11/03/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

|                              |                 |              |
|------------------------------|-----------------|--------------|
| <b>Office Action Summary</b> | Application No. | Applicant(s) |
|                              | 09/574,327      | MAN ET AL.   |
| Examiner                     | Art Unit        |              |
| Fritz Alphonse               | 2133            |              |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1) Responsive to communication(s) filed on 06 July 2004.

2a) This action is FINAL.                    2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4) Claim(s) 1-3, 7-10, 12-22 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 1-3,7-10 and 12-22 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 19 May 2000 is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some \* c) None of:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.

3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_.

4) Interview Summary (PTO-413) Paper No(s) \_\_\_\_\_.

5) Notice of Informal Patent Application (PTO-152)

6) Other: \_\_\_\_\_

## DETAILED ACTION

### *Claim Rejections - 35 USC § 103*

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-3, 7- 9, 11-17, 19-24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hung (U.S. Pat. No. 4,894,718) in view of Perez (6,323,828).

As to claim 1, Hung (fig. 1) shows a method of testing digital graphics data, which provides digital graphics data (note in fig. 1 Hung shows a digital graphics provided by UUT (11, 10) which clearly has digital signal). Hung (fig. 1) shows a method of testing which receives, at a test apparatus, the digital graphics data from a digital graphics port of the graphics controller under test (note the digital graphics data is received by the video signals selection multiplexer 1); and calculates at the test apparatus, a characteristic value that is based upon the digital graphics data (the characteristic is calculated at CRC circuit 3); and sensing the calculated characteristic.

Hung does not explicitly provide ports or interface to the graphics controller.

However, in the same field of endeavor, Perez (Fig. 2) discloses a system comprising test apparatus having a connector for connection to the video output

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connector of a computer under test. In addition, Perez discloses a DDC interface (display data channel), which is a serial interface (col.2, lines 59-67; col. 4, lines 18-20).

Therefore, it would have been obvious, to one of ordinary skill in the art at the time of the invention, to modify Hung's system (note Hung's cyclic redundancy check circuit is connected to an expansion slot of the computer and the video output) by specifically providing a system comprising test apparatus having a connector, as disclosed by Perez (see col. 1, lines 23-33). Perez notifies that another way is to use the test unit in a stand-alone process and return a serial signal (col. 3, lines 1-9). Doing so would provide a simple and relatively cheap arrangement for testing the video output of a computer.

As to claims 2-3, Hung (fig. 1) shows a method, wherein the expected characteristic is a calculated value based upon the predetermined type of digital graphics data, and wherein the predetermined type of digital graphics data includes at least one of a red, green, and blue color component (note the RGB and R'G'B' values representing the video signals; col. 2, lines 18-24); and wherein the predetermined type of digital graphics data includes a horizontal synchronization component (note the horizontal sync signal Hs; see col. 2, lines 29-35).

As to claims 7-8, Hung discloses a method, wherein the expected characteristic is a circular redundancy check (CRC) value, and wherein the predetermined type of digital graphics data is selectable (col. 2, lines 25-28).

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As to claim 9, Hung (fig. 1) teaches about a method, wherein the step of providing the calculated characteristic from a test apparatus to a serial interface includes providing the calculated characteristic to the computer (the characteristic is calculated at CRC circuit 3).

Hung does not explicitly disclose the step of comparing the calculated characteristic to the expected characteristic by at least one of the computer and the graphics controller.

However, this limitation is disclosed by Perez (col. 1, lines 48-61). See the motivation above.

As to claims 12 and 16, Hung does not teach that the graphics output port includes an output port for a flat panel display. However, the use of graphics output port including an output port for a flat panel display is obvious and very well known in the art.

As to claims 13 and 15, Perez discloses a method, wherein the serial interface is associated with the graphics output port (col. 4, lines 10-20; see figure 2).

As to claim 14, the claim has substantially the limitations of claim 1, therefore, it is analyzed as previously discussed in claim 1 above.

As to claim 17, Hung discloses a method, wherein the steps of determining and providing occur in real-time with respect to the step of receiving (note that Hung

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discloses that the output of the delay circuit 12, enters the CRC circuit 3 at the same time as the sync clock 26 enters the CRC circuit 3). See col. 4, lines 20-25

As to claim 19, Hung (fig. 1) shows an apparatus for testing digital graphics data, the system comprising: a graphics data analyzer module having an input coupled to a connector to receive digital graphics data sent from a graphics controller (note the video signals selection multiplexer 1 receives the video signals from the video signals connector 10 on the unit under test (UUT 11), and an output (col. 2, lines 64 through col. 3, line 9), said graphics data analyzer being capable of calculating a value from digital graphics data it receives through the connector.

Hung does not explicitly disclose a serial bus interface control module having an input coupled to the output of the graphics data analyzer module, and a serial data port coupled to the connector. However, these limitations are disclosed by Perez (figs. 1-2; col. 1, lines 48-61; col.2, lines 54-67). See the motivation above.

As to claims 20-22, the claims have substantially the limitations of claims 12 and 15, therefore, they are analyzed as previously discussed in claims 12 and 15 above.

3. Claims 10 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hung in view of Perez as applied to claims 1 and 14 above, and further in view of Applicant Admitted Prior Art (Digital Visual Interface DVI).

As to claims 10 and 18, Hung does not teach about a method, wherein the step of receiving, at a test apparatus includes receiving at a test apparatus the graphics data at a

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rate greater than 100 MHZ. However, this is obvious and very well known in the art, as disclosed by Applicant Admitted Prior Art (Digital Visual Interface DVI). See DVI (page 11, section 2.2.2. line 1-7).

***Response to Arguments***

4. Applicant's arguments filed 5/6/2004 have been fully considered but they are not persuasive.

Applicant's argues on page 6, that Hung reference is not a method for testing digital graphics data and does not provide digital graphics data to a digital graphics port.

The Examiner respectively disagrees. Hung does provide digital graphics data to a digital graphics port (note in figure 1, the bit data provided are digital graphics data).

Applicant's argues on page 7 that the video signals of Perez, like those of Hung, are analog signals.

The examiner respectfully disagrees. Perez (col. 2, lines 59-65) teaches generating the results of a test back to the unit under test over a digital display channel within a VGA connector. In addition, Perez (see col. 2, lines 54-67) teaches about "DDC (data display channel) interface" which is a serial interface serving as a digital port for transmitting data (col. 4, lines 18-20). The DDC interface is very well known in the art as a port (see "Port" and "Interface" as defined by McGraw-Hill electronics Dictionary). In addition, the DDC interface is a serial interface as evidenced by AN610 (page 1); Gu

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(U.S. Pat. No. 6,505,266) col. 3, line 25; Dalgleish et al. (U.S. Pat. No. 6,373,476) col. 1, line 38; Marsanne et al. (U.S. Pat. No. 5,884,044) col. 1, line 6 and 13.

#### Conclusion

5. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

**or faxed to:** (703) 872-9306 for all formal communications.

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Fourth Floor (Receptionist).

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fritz Alphonse, whose telephone number is (571) 272-3813. The examiner can normally be reached on M-F, 8:30-6:00, Alt. Mondays off.

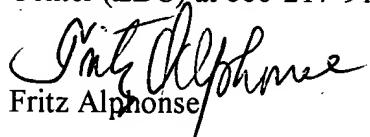
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert De Cady, can be reached at (571) 272-3819.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.

Information regarding the status of an application may also be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For

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more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
Fritz Alphonse

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October 28, 2004



Guy J. LAMARRE  
PRIMARY EXAMINER